

PATENT APPLICATION
Docket No. 8750-057
Client No. SPX200303-009US

Applicant: Sang-Eun Lee and Yun-Heub Song
Filing Date: February 5, 2004 Art Unit: not yet assigned
Serial No.: not yet assigned Examiner: not yet assigned
For: METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT
USING A SELECTIVE DISPOSABLE SPACER TECHNIQUE AND SEMICONDUCTOR
INTEGRATED CIRCUIT MANUFACTURED THEREBY

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
<u>S</u>	_____	6,043,537	3/28/2000	Jun, et al.		

Examiner: Sang-Eun Lee

Date Considered: 4/6/05